

FIG. 1

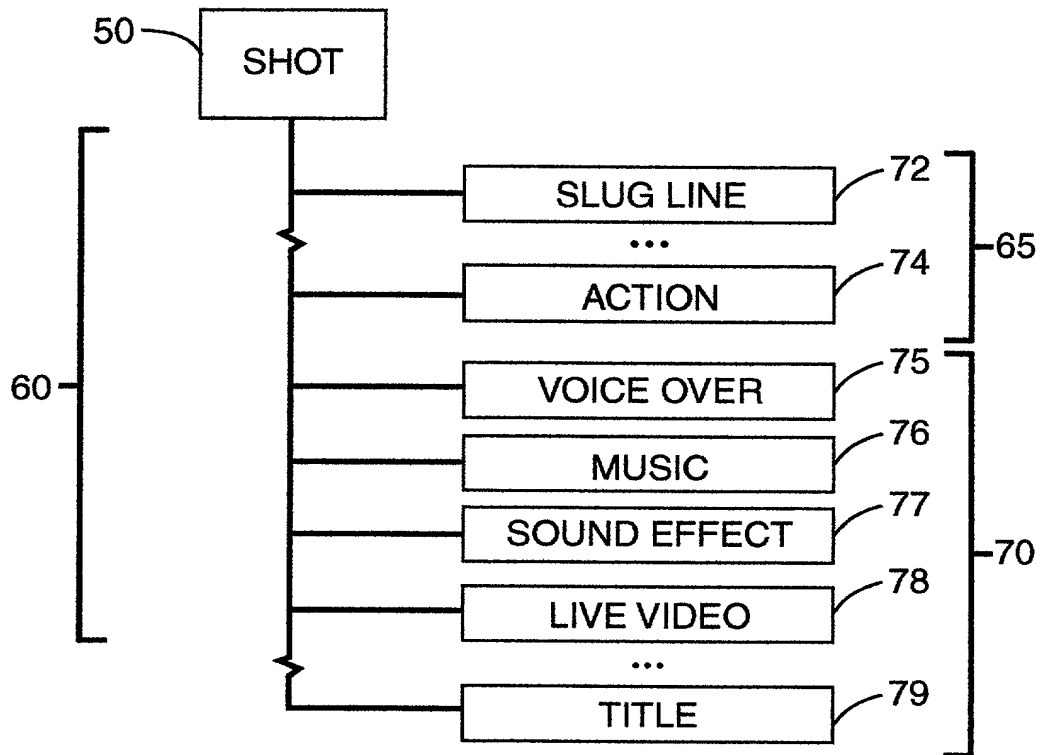


FIG. 2

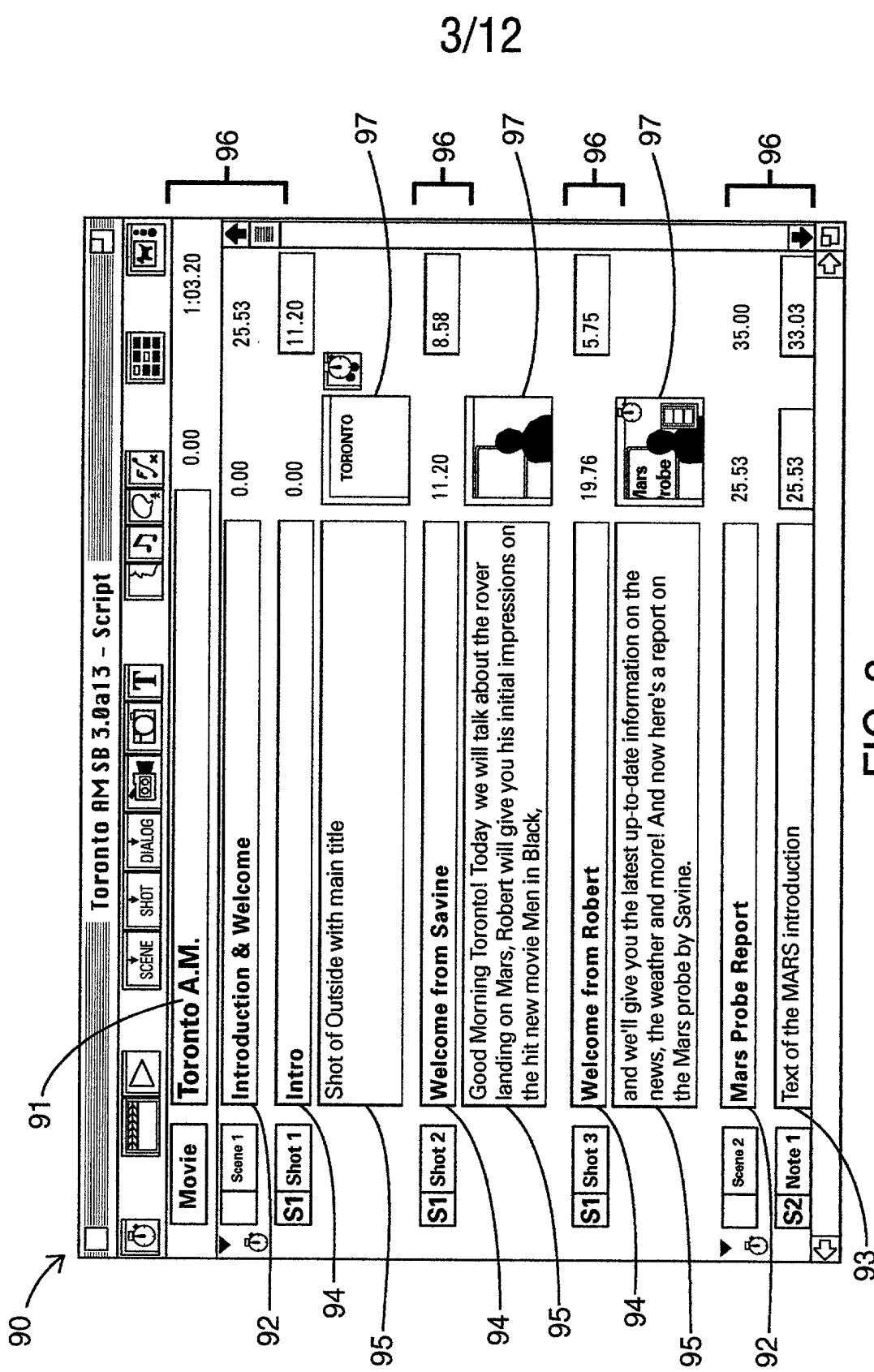


FIG. 3
Prior Art

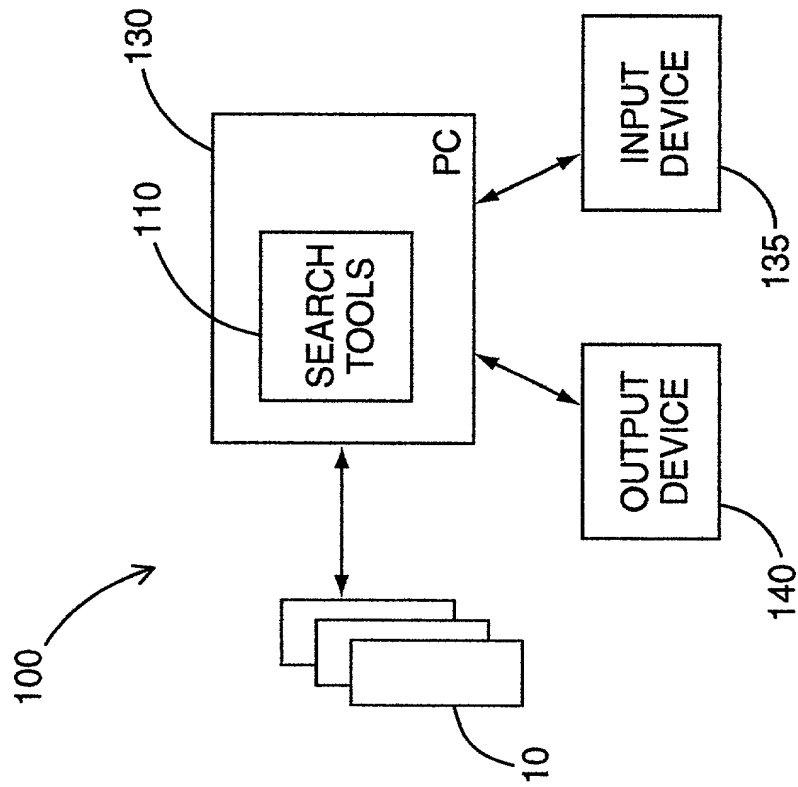


FIG. 4A

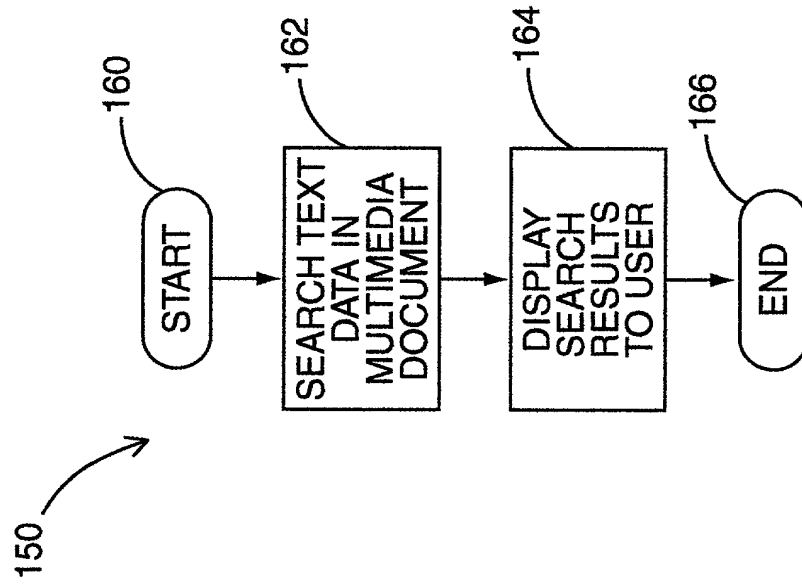


FIG. 4B

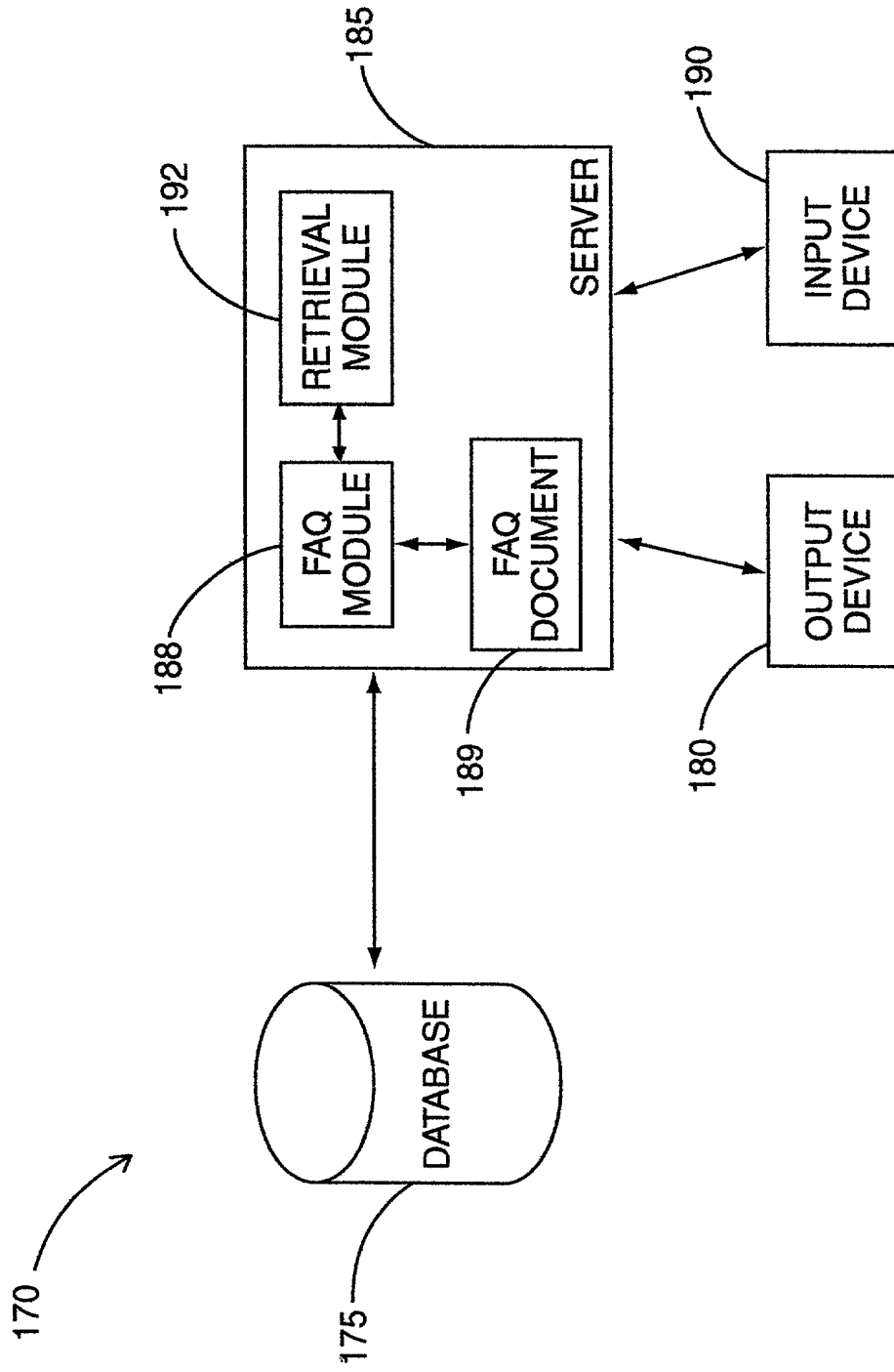


FIG. 5A

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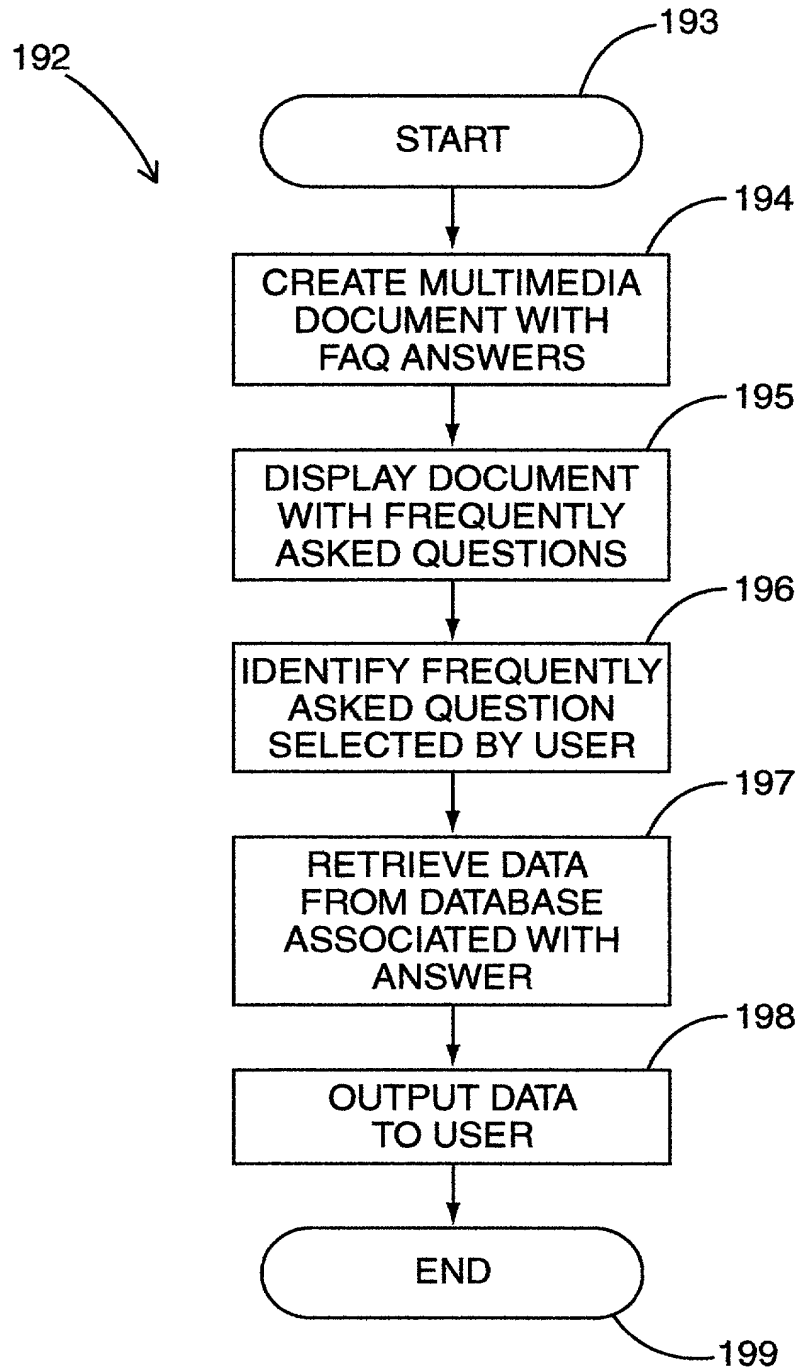


FIG. 5B

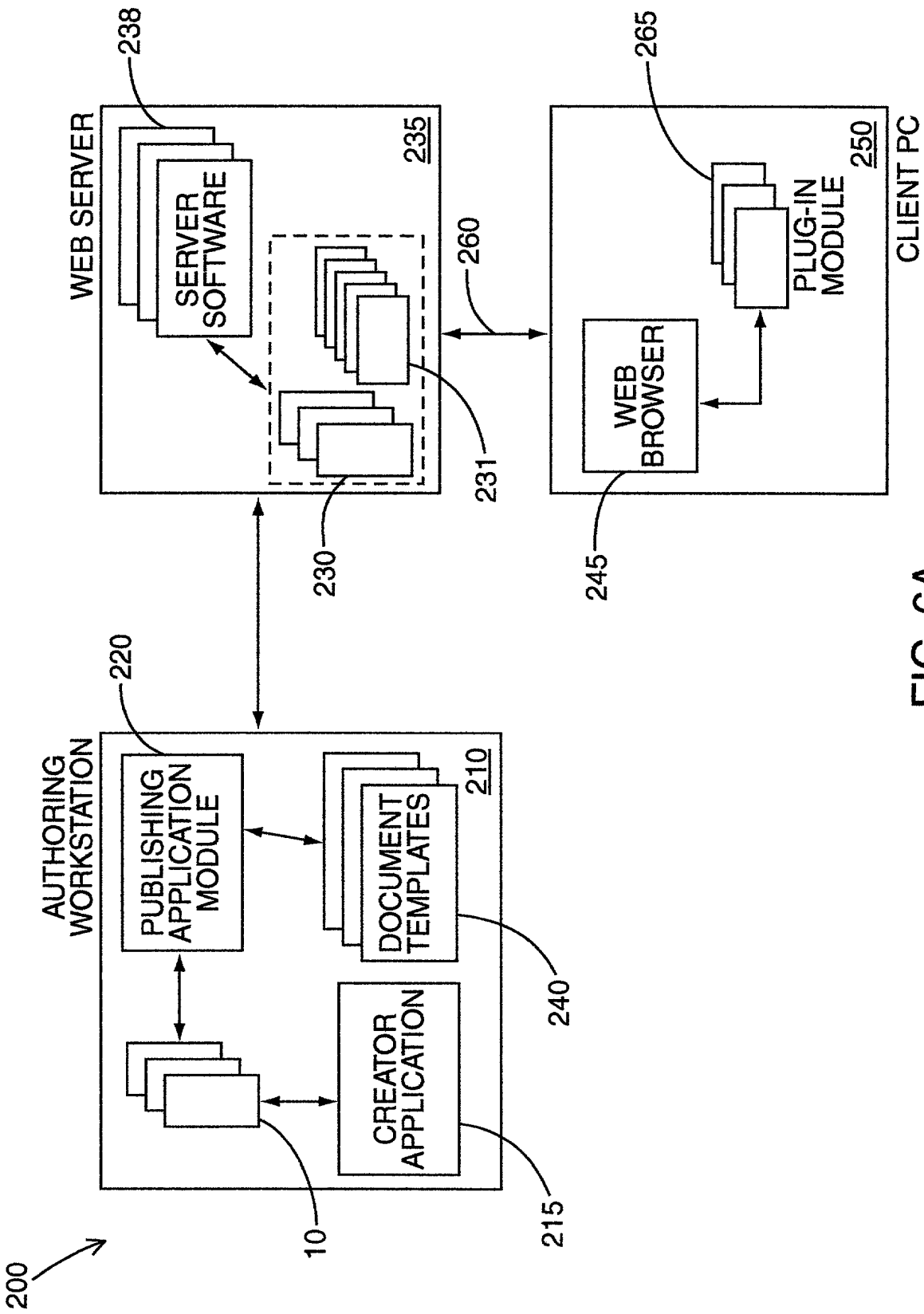


FIG. 6A

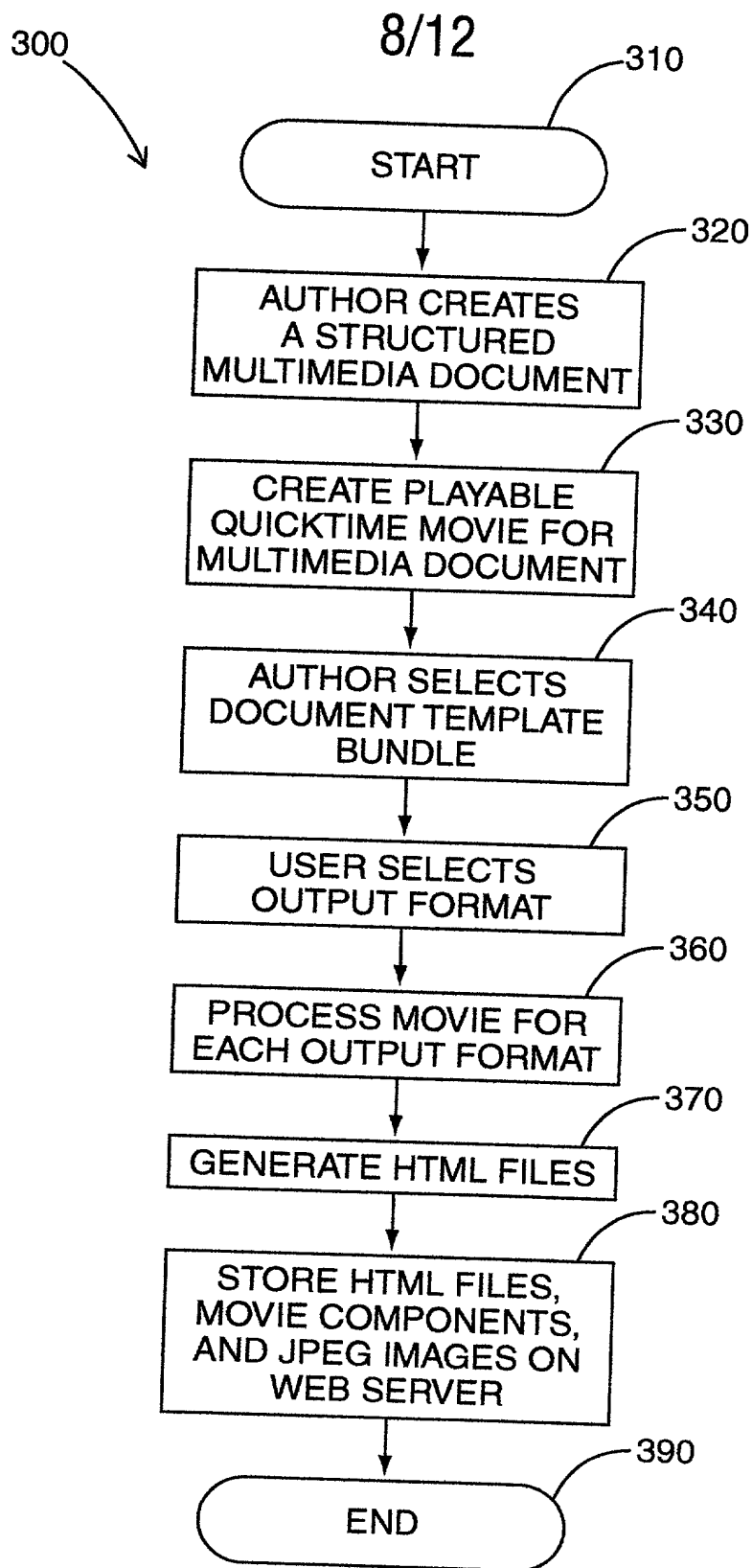


FIG. 6B

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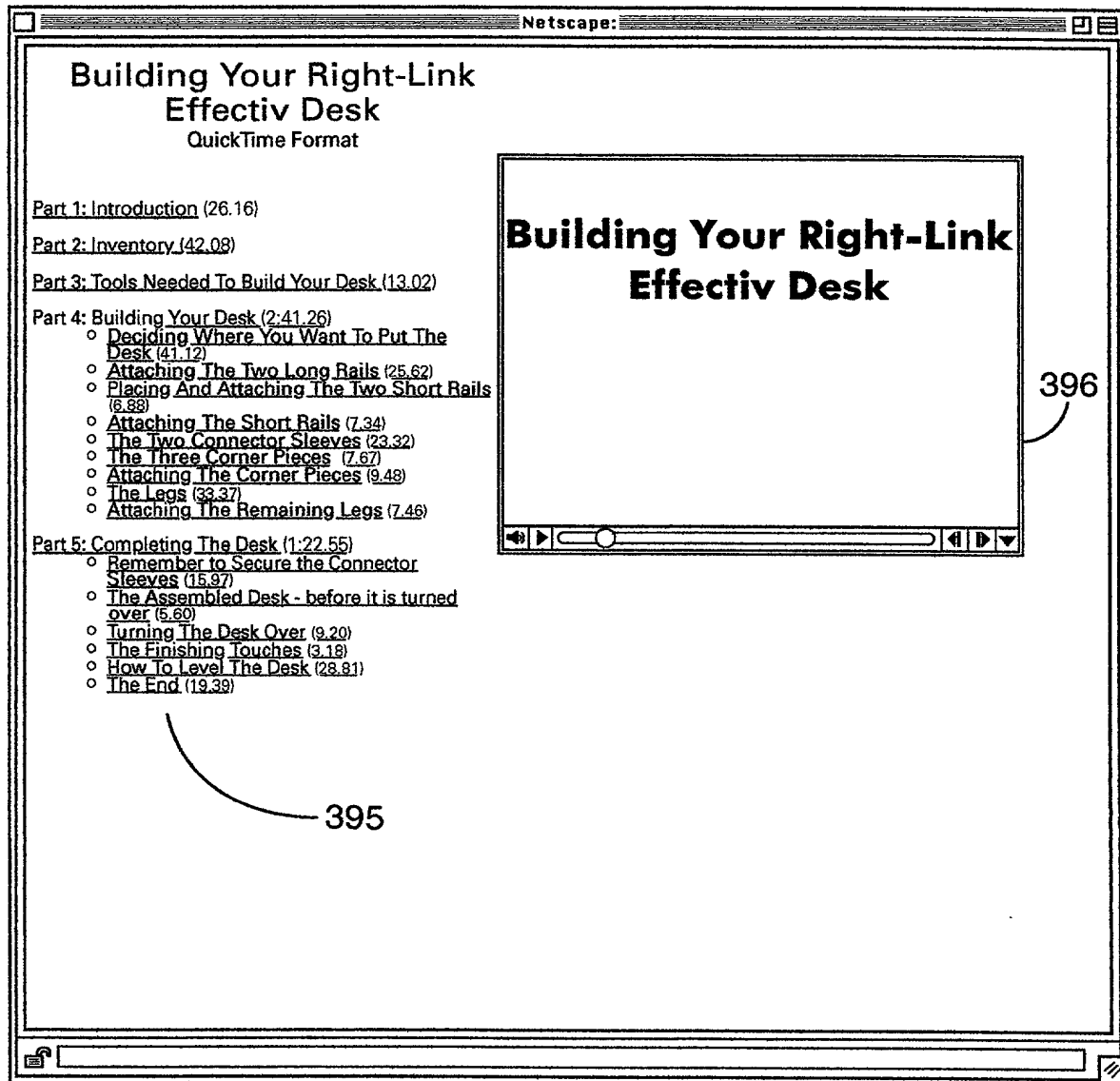
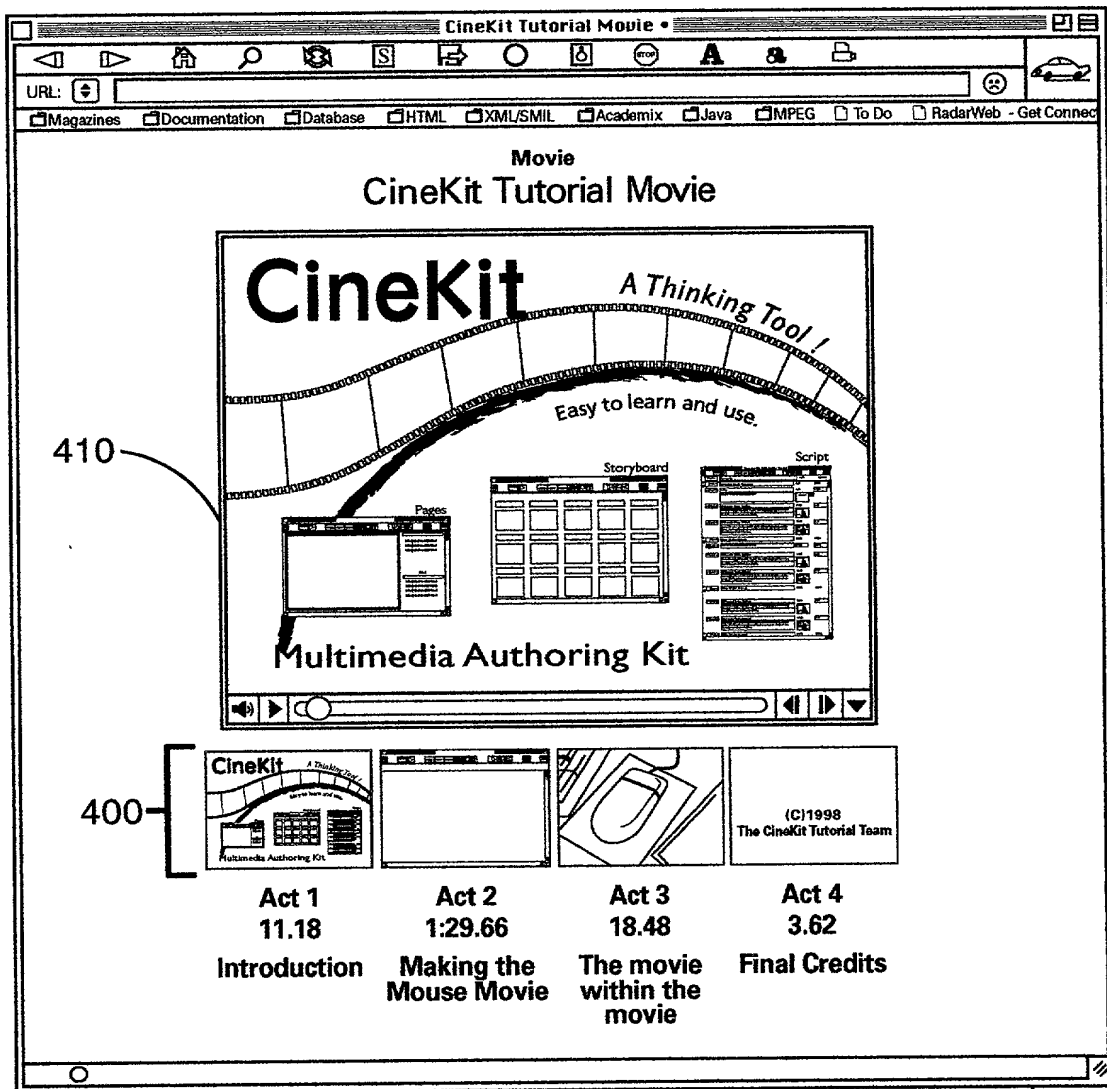


FIG. 7

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FIG. 8

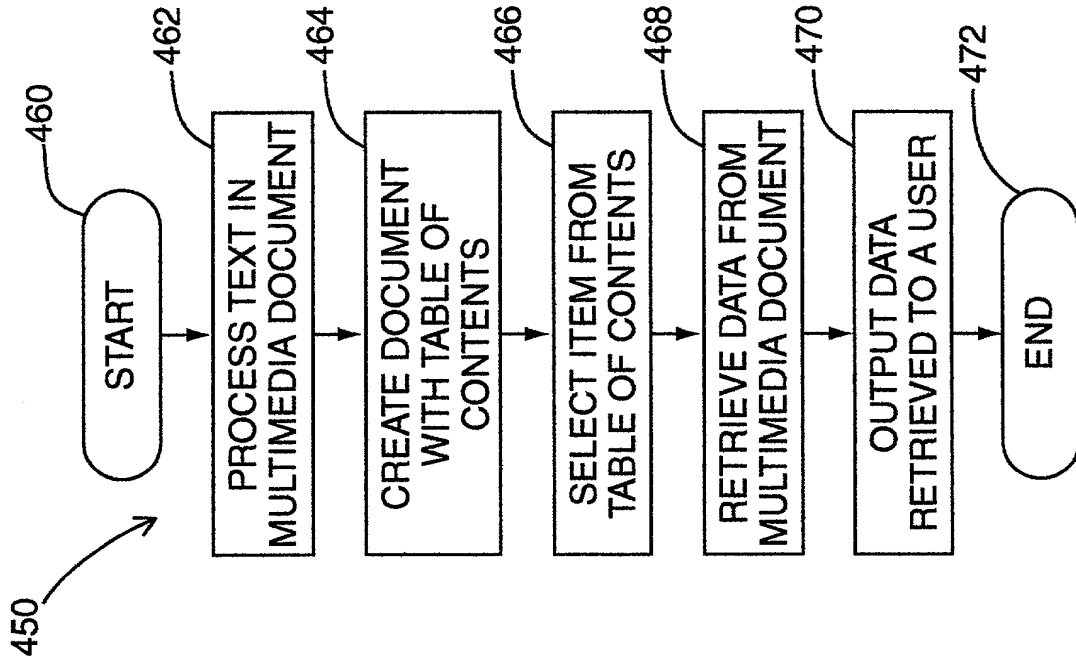


FIG. 9B

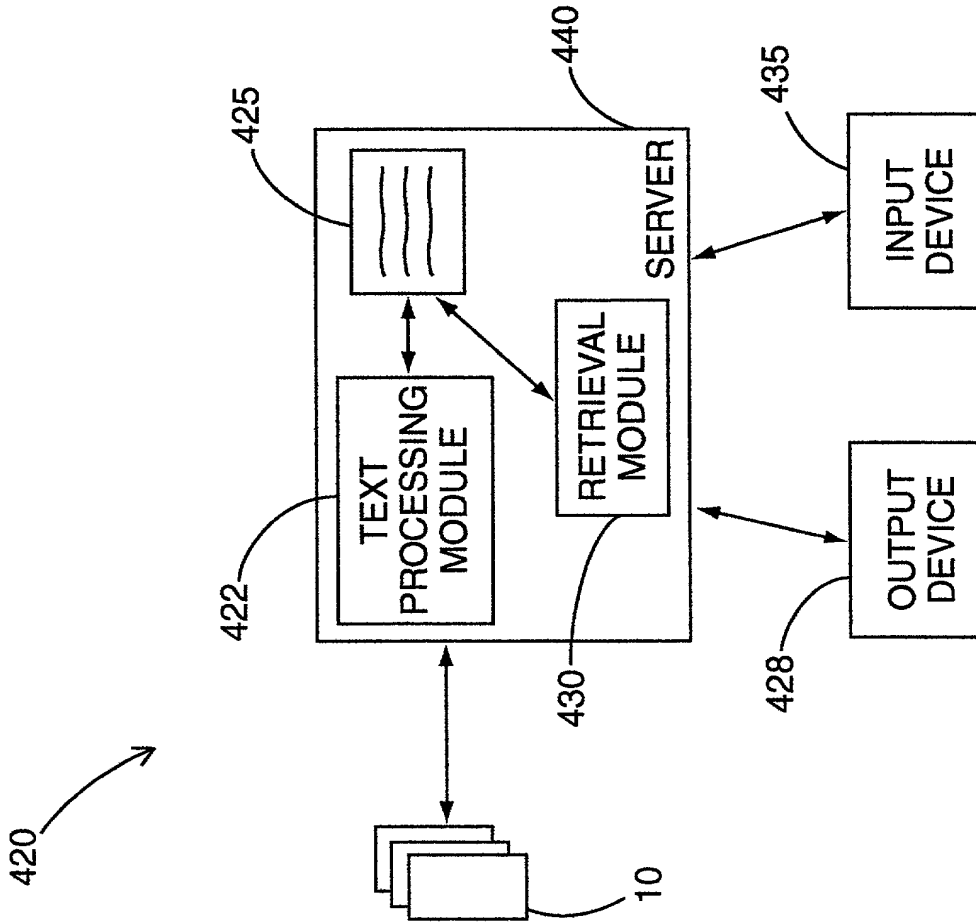


FIG. 9A

FIG. 10A is a block diagram of a system 480. The system 480 includes a PC 484, an input device 486, an output device 485, and a storage device 10. The PC 484 includes a retrieval module 482 and an update module 488. The storage device 10 is connected to the retrieval module 482. The input device 486 and output device 485 are connected to the PC 484.

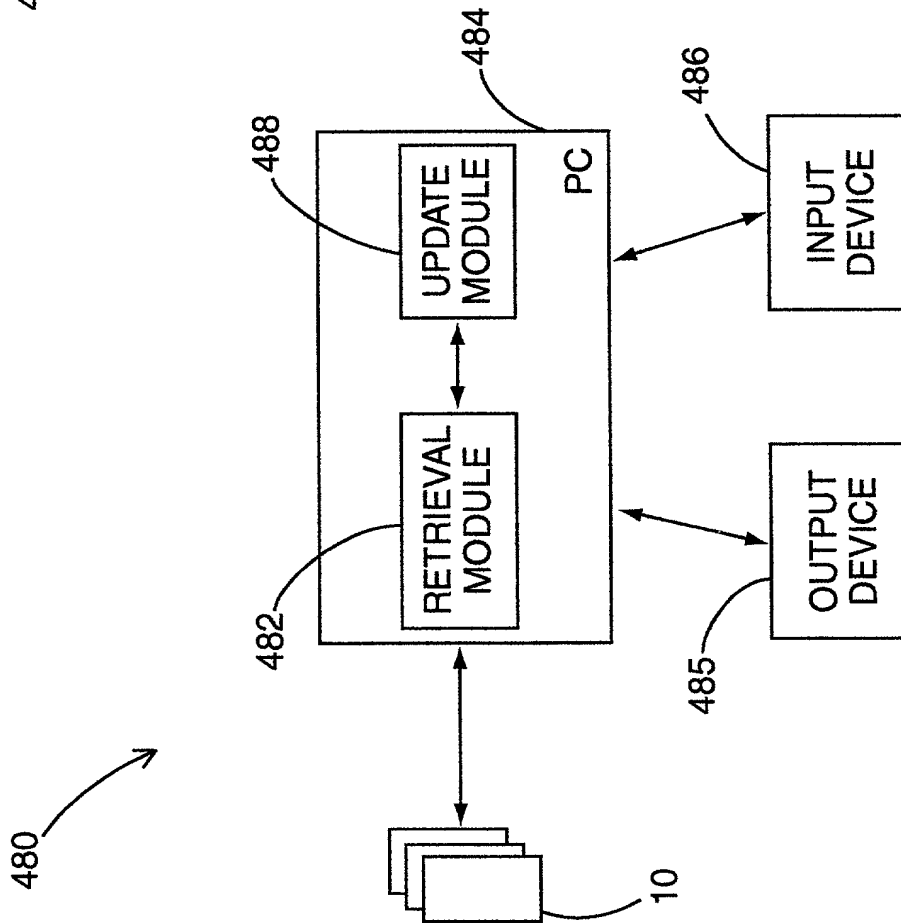


FIG. 10A

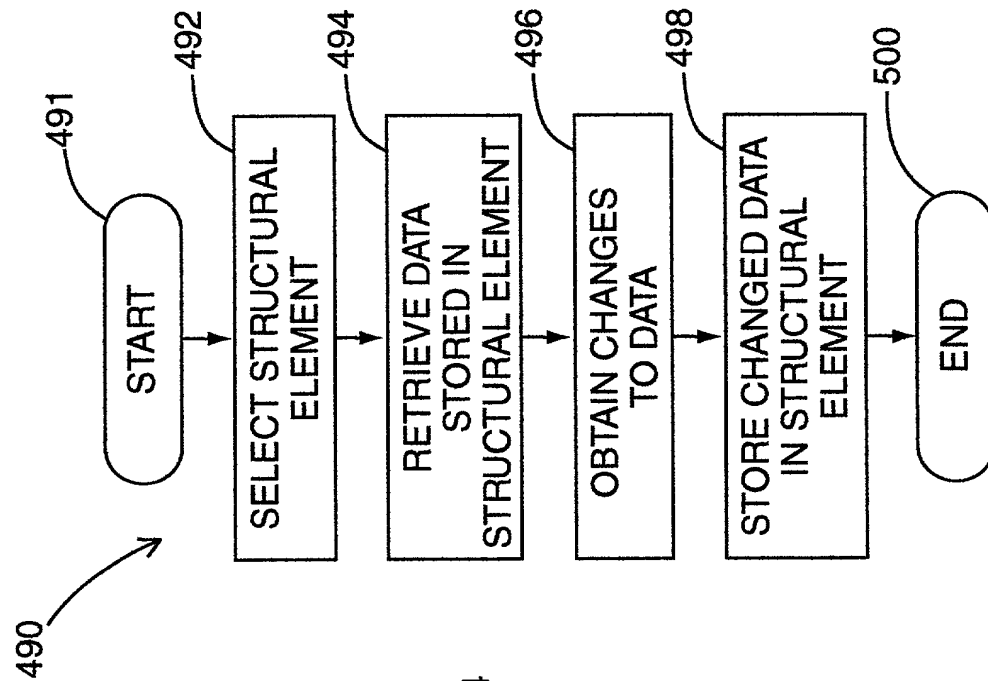


FIG. 10B